

DATA SHEET

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Headquarter

Opticis Co., Ltd
501, ByucksanTechnopia, 434-6
Sangdaewon-Dong, Chungwon-Ku,
Sungnam City, Kyungki-Do, 463-120
South Korea
Tel: +82 (31) 737-8033~9
Fax: +82 (31) 707-8079

www.opticis.com

North American Office

Opticis North America Inc.

330 Richmond Street, Suite 100, Chatham,
Ontario N7M 1P7
Canada

Tel: +1 (519) 355-0819
Fax: +1 (519) 355-0502

850 nm VCSEL LC Transceivers

Description

Opticis M3-xxx-Lyz is a fibre optic transceiver which meets the specifications defined in Gbit Ethernet and Fibre Channel. The transceiver offers a simple and convenient way to interface for Gbit Ethernet switch system running up to 1.25Gbps and Fibre Channel system running up to 1.0625Gbps with multimode fibre optic cables.

All modules satisfy Class I Laser Eye Safety requirements in accordance with the US FDA/CDRH and international IEC-825 standards.

The transmitter and receiver functions are contained in a two-row, 10-pin (2x5) package with a Duplex LC receptacle. The transmitter employs a high performance 850 nm Vertical Cavity Surface Emitting Laser (VCSEL) with a driver circuit, which converts Positive Emitter Coupled Logic (PECL) data to light. The receiver incorporates a GaAs PIN photodiode converting the light signal into an electrical current, which is amplified and regenerated into PECL-compatible data. A Signal Detect status output is also provided in the receiver.

The transceiver is operated by dual +3.3V & +5.0V power supply over 0°C to +70°C. The transceiver package is made of metal (Duplex-LC version) for good EMI shielding.

Features

- ◆ Comply with IEEE 802.3z Gigabit Ethernet and Fibre Channel Std.
- ◆ VCSEL (vertical cavity surface emitting laser) is adopted
- ◆ IEC 60825-1 Class 1 Laser Eye Safety
- ◆ Industry Standard 2x5 package style
- ◆ Duplex LC connector
- ◆ Dual +3.3V & +5.0V Power Supply Operation with PECL logic data interface
- ◆ Compatible with commercial Gigabit Ethernet and Fibre Channel PHY transceiver chipsets
- ◆ Metallic package is employed to reduce EMI
- ◆ Operating range for 1000BASE-SX is 550 meter with 50/125 μ m and 275 meter with 62.5/125 μ m

Equipments

- ◆ Switches
- ◆ Hubs
- ◆ Routers
- ◆ Media converters and Repeaters

Applications

- ◆ Switch to switch Interface
- ◆ Switch to server links

Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
Storage Temperature	T _{stg}	- 40	+ 85	°C
Supply Voltage	V _{CC}	- 0.3	+ 5.5	V
Input Voltage	V _{in}	-0.3	V _{CC}	V
Relative Humidity	RH	5	85	%
Lead Soldering Temperature & Time	-	-	260°C, 10 sec	

Recommended Operating Conditions

Parameter	Symbol	Minimum	Typical	Maximum	Units
Ambient Operating Temperature	T _A	0		+ 70	°C
Data Output Load	R _{LD}		50		Ω
Power Supply Rejection ¹	PSR		50		mV _{pp}
Supply Voltage	V _{CC}	+3 +4.5	+3.3 +5.0	+3.6 +5.5	V

¹ Tested with a 50mV_{p-p} sinusoidal signal in the frequency range from 500 Hz to 500 MHz on the V_{CC} supply with the recommended power supply filter in place. Typically less than a 0.25 dB change in sensitivity is experienced.

Transmitter Electrical Interface (over Operating Temperature Range)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Data Input Load	T _{LD}		50		Ω
Differential Input Voltage Swing	V _{ID}	0.4		1.6	V

Receiver Electrical Interface (over Operating Temperature Range)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Data Output Voltage Swing (Peak-to-Peak)	V _{Op-p}	0.6	-	0.8	V
Output High Voltage	V _{OH}	V _{CC} -1.1		V _{CC} -0.9	V
Output Low Voltage	V _{OL}	V _{CC} -1.84		V _{CC} -1.62	V

Electrical Power Supply Characteristics (over Operating Temperature Range)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Supply Voltage	V _{CC}	3.1 4.5	3.3 5.0	3.6 5.5	V
Supply Current	TX	I _{CT}	60	70	mA
	RX	I _{CR}	90	100	mA

Transmitter Performance Characteristics (over Operating Temperature) 50 μ /125, 62.5 μ /125 multi mode GOF

Parameter	Symbol	Minimum	Typical	Maximum	Units
Data Rate ¹	B	-	1.25	-	Gb/s
Average launch Power ²	P _o	-7	-	0	dBm
Wavelength	λ_c	840	850	860	nm
Spectral Width (RMS)	$\Delta\lambda_{RMS}$	-	-	0.85	nm
Coupled Power Ratio ³	CPR	9	-	-	dB
Extinction Ratio		9	10	-	dB
Output Rise Time ⁴	T _{TLH}	-	-	0.26	ns
Output Fall Time ⁴	T _{THL}	-	-	0.26	ns
Relative Intensity Noise	RIN	-	-	-117	dB/Hz

1 Transmitter is shut off when transmitting continuous "zero". Power supply does not need to be reset to reactivate the transmitter. Transmitter is also shut off when transmitting continuous "one". However, power supply needs to be reset to reactivate the transmitter.

2 CPR measured per TIA/EIA OFSTP-14A with valid 8B/10B data pattern

3 Measured from 20% to 80% levels on rising and falling edge of transmitted waveform.

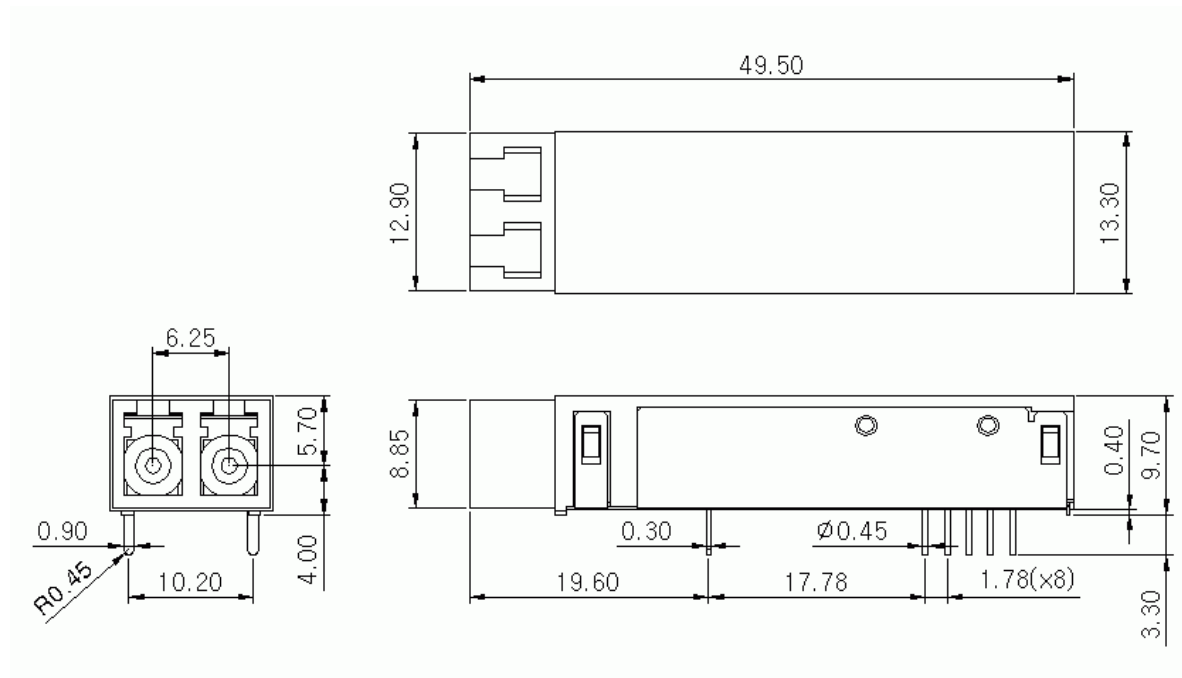
Receiver Performance Characteristics (over Operating Temperature) 50 μ /125, 62.5 μ /125 multi mode GOF

Parameter	Symbol	Minimum	Typical	Maximum	Units
Data Rate	B	-	-	1.25	Gb/s
Receive Sensitivity (10 ⁻¹² BER)	-	-24	-22	-17	dBm
Average Receive Power	-	-	-	0	dBm
Power budget		-17			dB
Stressed Receiver Sensitivity	50um Fiber	-	-13.5	-	dBm
	62.5um Fiber	-	-12.5	-	dBm
Output Rise Time ¹	T _{RLH}	-	-	0.25	ns
Output Fall Time ¹	T _{RHL}	-	-	0.25	ns
Wavelength of Operation	λ	600	850	900	nm
Return Loss ²	-	12	-	-	dB

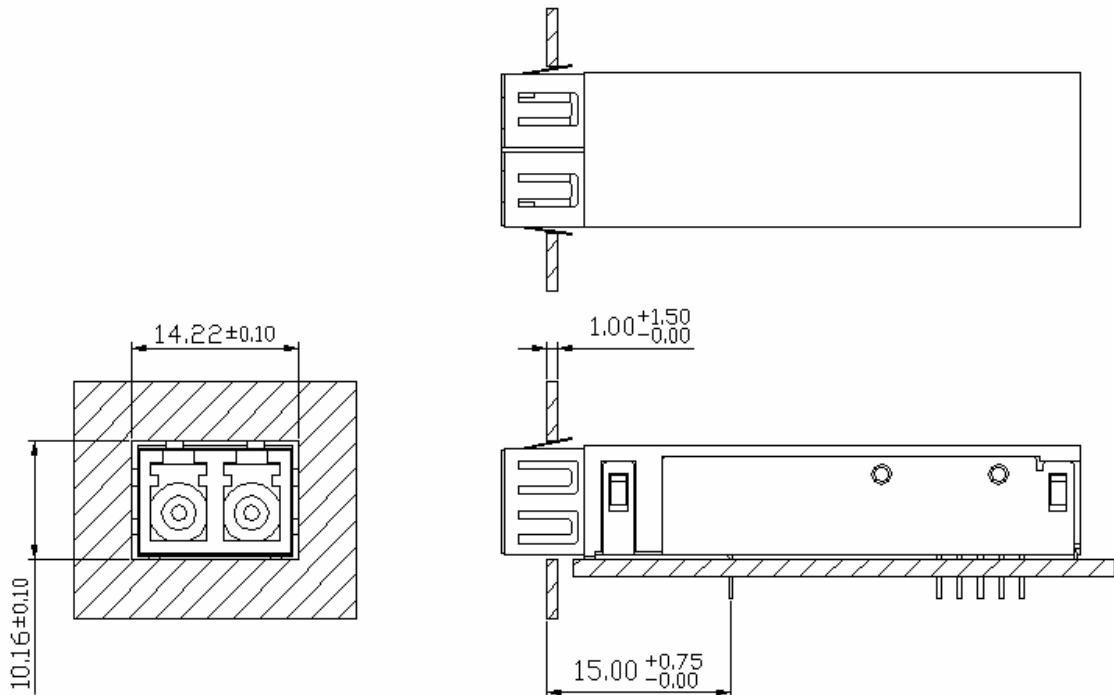
1 Measured from 20% to 80% levels on rising and falling edge of received waveform

2 Return loss is defined as the minimum attenuation (dB) of received optical power for energy reflected back into the optical fiber.

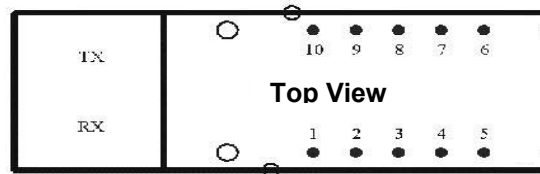
Mechanical Dimensions [mm]



◆ **Shield**



Pin Assignments



Pin	Symbol	Functional Description
1	V_{EER}	Receiver Signal Ground Directly connect this pin to receiver signal ground plane.
2	V_{CCR}	Receiver Power Supply Provide +3.3V via the recommended receiver power supply filter circuit. Locate the power supply filter circuit as close as possible to the V_{CCR} pin.
3	SD	Signal Detect Signal Detect is a single-ended TTL output. (ECL output available) Normal optical input levels to the receiver result in a logic "1" output, V_{OH} , asserted. Low input optical levels to the receiver result in a fault condition indicated by a logic "0" output V_{OL} , de-asserted. If Signal Detect output is not used, leave it open-circuited.
4	RD-	Receiver Data Out-Bar AC coupled - PECL compatible
5	RD+	Receiver Data Out AC coupled - PECL compatible
6	V_{CCT}	Transmitter Power Supply Provide +3.3V via the recommended transmitter power supply filter circuit. Locate the power supply filter circuit as close as possible to the V_{CCT} pin.
7	V_{EET}	Transmitter Signal Ground Directly connect this pin to the transmitter signal ground plane.
8	TDis	Transmitter Disable
9	TD+	Transmitter Data In AC or DC coupled - PECL compatible
10	TD-	Transmitter Data In-Bar AC or DC coupled - PECL compatible

Ordering information

M3-XXX-ABC

XXX: **015** stands for 155Mbps data rate.

106 and **125** stand for 1.0615Gbit data rate for Fibre Channel
and 1.25Gbit data rate for Gbit Ethernet, respectively.

212 and **250** stand for 2.125Gbit data rate for Fibre Channel
and 2.5Gbit data rate for Gbit Ethernet, respectively.

A : Connector style, that is, **S** for SC and **L** for LC and **P** for SFP.

B : Receiver termination, that is, **A** for AC coupling and **D** for DC coupling

C : Signal Detect Level, that is, **P** for PECL and **T** for TTL.

For 155Mbs, C represents high voltage (+5V) as **H** and low voltage (+3.3V) as **L**.

Note: LC module has only alternative of **TTL SD_out** and **AC coupling**.

155M module has only alternative of **PECL SD_out**, **DC coupling** and **SC connector**.

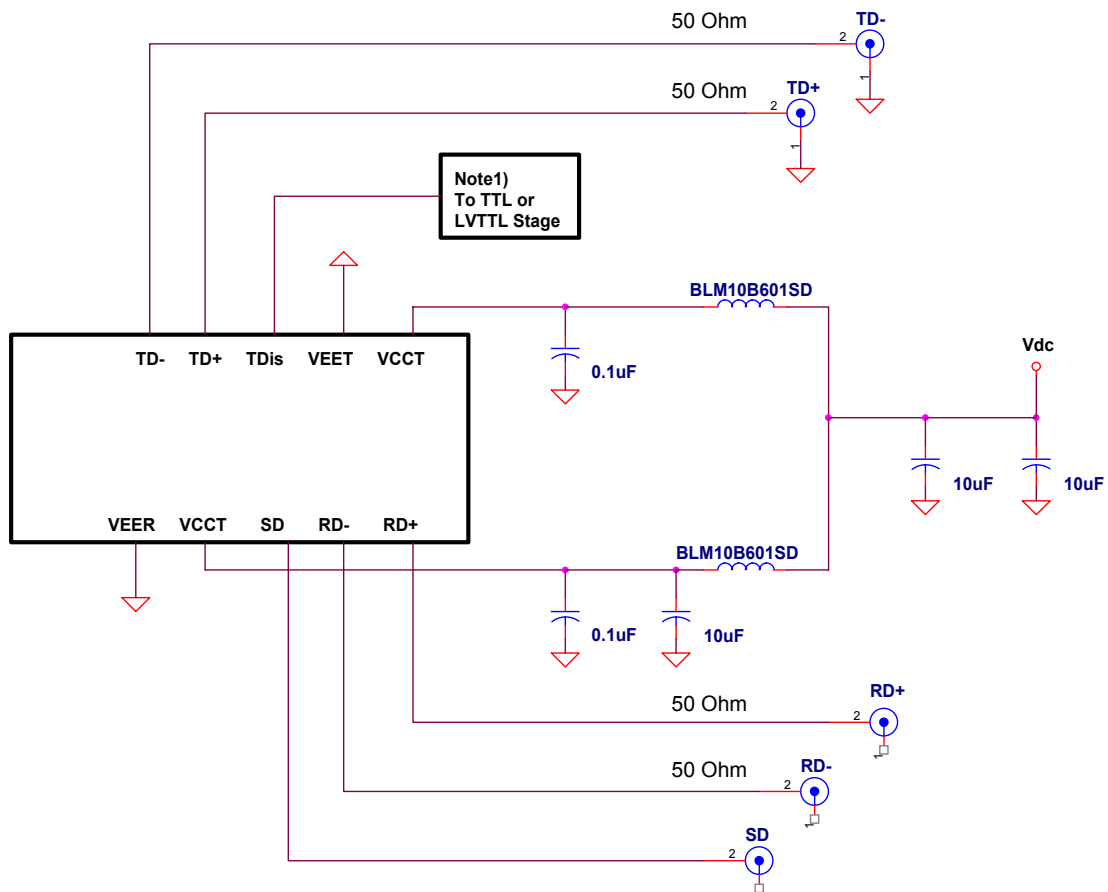
Application Notes

Transmitter : When the Tdis is Low or Floating and TD+ input is at logic HIGH and TD- input is at logic LOW, the LD is ON, and vice versa. Transmitter input is AC coupled. The center value for the input voltage is set by the user and can be ECL, PECL, etc.

Receiver: Both differential RD+ and RD- outputs are AC coupled. For optimum performance, both outputs should be terminated in the same manner, even if only one is used.

The SD circuit monitors the level of the incoming optical signal and generates a logic LOW signal when insufficient photocurrent is produced.

Interface circuit: The power supply line should be well filtered. All 0.1 μ F power supply bypass capacitors should be as close to the M3-125-LAT transceiver modules as possible (Opticis 2X5 evaluation board recommended). The two front GND posts should be grounded to system ground.



Note1) Transmitter disable : TTL (5V) or LVTTTL(3.3V)

Normal Operation : Logic "0", or Open Circuit(Floating) → LD ON

Transmitter Disable : Logic "1" → LD Off

*** Transmitter can be disabled by connecting Pin8 to VCCT via a 500 Ohm resistor.

This input pin requires a minimum of 50 μ A for it to operate.